

ABSTRACT OF THE DISCLOSURE

An electronic circuit comprising a first counter clocked by a clock signal provided to have a first period and provided by an oscillator external to the circuit, and comprising a second counter clocked with a second period by an oscillator internal to the circuit, the second counter being reset each time the content of the first counter is a multiple of a first predetermined value, and a means for activating an alert signal when the second counter reaches a second predetermined value such that the product of the second predetermined value by the second period is greater than the product of the first predetermined value by the first period.

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